PMSM low voltage ride through based on Coordinated Control Strategy

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Abstract. Based on the analysis of the operation characteristics of the fault of direct-drive permanent magnet wind turbine and the existing protection strategies, in order to improve the low-voltage operation capability of direct-drive permanent magnet wind turbine, a new coordinated control strategy for low-voltage crossing is proposed in this paper, which includes DC brake system control of unloading circuit, double second-order generalized integral phase-locked control, extracting positive and negative sequence of grid voltage and reactive power control on grid side. Based on the common three-phase symmetrical drop fault and asymmetrical fault caused by single grounding short circuit in power grid, PASCAD simulation experiment is conducted to verify the low voltage traversing ability of wind turbine.

1 Introduction

At present, instantaneous voltage sag faults of wind power node account for a large proportion of wind power engineering faults^[1]. If the grid voltage sag occurs and the wind generator is automatically trawled without control, it will lead to a large area of power failure and even the risk of local grid failure^[2]. Therefore, countries have put forward very strict fault crossing standards.When the power grid voltage falls, the system is required to operate without going off the grid. Even the wind farm should have a certain reactive power compensation ability and be able to carry out grid-connected operation in accordance with the specified standard time requirements when the power grid fails^[3]. The purpose of this paper is to make reasonable use of the advantages of permanent magnet synchronous wind turbine, and then design a low voltage crossing scheme that can ensure the continuous network operation of PMSG, which make an accurate and rapid response when the grid voltage drops. The research on PMSG low voltage crossing technology is of great significance, which can promote the large-scale development of wind power and improve the operation performance of wind power networking^[4].

2 Control Strategy of DC Braking System Based on Unloading Circuit

In accordance with China's low voltage traverse requirements for wind power system, it is necessary to ensure that the wind power generation system can operate on the grid for a period of time after voltage drop^[5], and the charge of the capacitor can be quickly removed. At the same time, it must control the grid-side inverter to compensate the reactive power of the system to a certain

extent, and realize the low voltage through operation of the wind power generation system with the method of joint coordination control.

Fig. 1 is the control schematic diagram of the unloading protection circuit. When the system runs stably, the unloading circuit does not work; In the event of a grid failure, the power input from the rectifier side Ps is proportional to the power absorbed by the wind turbine. But the side feed into the grid inverter power will collapse because of a malfunction, resulting in a unbalanced power ΔP . When ΔP exceeds the threshold Δ Pt, ΔP after PI regulator control pulse width regulator output signal. Put into the unloading circuit to release the unbalanced power of this part. A hysteresis is set in PI regulator, so they don't frequent at point ΔP output signals, which reduces cutting action, reduces equipment loss and improves the system stability.



Fig.1 The schematic diagram of unloading circuit

3 Phase lock and positive and negative sequence voltage extraction

During normal operation, the grid-side inverter inverts the DC on both sides of the capacitor into three-phase electricity with the same amplitude and phase as the power grid. A large amount of reactive power will be generated at the grid side when the voltage of the parallel point drops instantly. At this time, in addition to putting in the unloading circuit, the grid-side inverter also needs to send

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out a certain amount of reactive power to achieve reactive compensation^[6]. According to the requirement of low voltage crossing, the reactive power emitted by the inverter must respond quickly.In order to realize the accurate action of inverter, a phase-locked loop (PLL) technique based on double order generalized integral is proposed to detect the voltage in parallel nodes.

The phase-locked loop (PLL) based on double-second order generalized integral is used to realize the phaselocked system of the grid-connected wind power inverter system, and the positive and negative sequence components of the asymmetric grid voltage can be detected. The transient symmetry component of the flockin loop is obtained by an adaptive filter based on a secondorder generalized integrator. According to the following

variation formula (1), the asymmetric voltage vector \boldsymbol{u}_{abc} is decomposed into instantaneous positive, negative and zero sequence components. The decomposition formula is shown in (2), (3) and (4) :

$$\boldsymbol{u}_{abc} = \boldsymbol{u}_{abc}^{\dagger} + \boldsymbol{u}_{abc}^{-} + \boldsymbol{u}_{abc}^{0} \tag{1}$$

$$\boldsymbol{u}_{abc}^{+} = \begin{bmatrix} \boldsymbol{T}_{+} \end{bmatrix} \boldsymbol{u}_{abc}^{+}; \begin{bmatrix} \boldsymbol{u}_{a}^{+} \\ \boldsymbol{u}_{b}^{+} \\ \boldsymbol{u}_{c}^{+} \end{bmatrix} = \frac{1}{3} \begin{bmatrix} 1 & a & a^{2} \\ a^{2} & 1 & a \\ a & a^{2} & 1 \end{bmatrix} \begin{bmatrix} \boldsymbol{u}_{a} \\ \boldsymbol{u}_{b} \\ \boldsymbol{u}_{c} \end{bmatrix}$$
(2)

$$\boldsymbol{u}_{abc}^{-} = \begin{bmatrix} \boldsymbol{T}_{-} \end{bmatrix} \boldsymbol{u}_{abc}^{-}; \begin{bmatrix} \boldsymbol{u}_{a}^{-} \\ \boldsymbol{u}_{b}^{-} \\ \boldsymbol{u}_{c}^{-} \end{bmatrix} = \frac{1}{3} \begin{bmatrix} 1 & a^{2} & a \\ a & 1 & a^{2} \\ a^{2} & a & 1 \end{bmatrix} \begin{bmatrix} \boldsymbol{u}_{a} \\ \boldsymbol{u}_{b} \\ \boldsymbol{u}_{c} \end{bmatrix}$$
(3)

$$\boldsymbol{u}_{abc}^{\boldsymbol{\rho}} = \begin{bmatrix} \boldsymbol{T}_{0} \end{bmatrix} \boldsymbol{u}_{abc}; \begin{bmatrix} \boldsymbol{u}_{a}^{\boldsymbol{\rho}} \\ \boldsymbol{u}_{b}^{\boldsymbol{\rho}} \\ \boldsymbol{u}_{c}^{\boldsymbol{\rho}} \end{bmatrix} = \frac{1}{3} \begin{bmatrix} 1 & 1 & 1 \\ 1 & 1 & 1 \\ 1 & 1 & 1 \end{bmatrix} \begin{bmatrix} \boldsymbol{u}_{a} \\ \boldsymbol{u}_{b} \\ \boldsymbol{u}_{c} \end{bmatrix}$$
(4)

T is the matrix transformation, a is the phase shift of the fundamental wave frequency added to the instantaneous sinusoidal input signal, which is equivalent to 120° phase shift, and is a special form of Fortescue operator. The research of controlling three-phase gridconnected converters mainly focuses on the positive sequence and negative sequence components of voltage.Each order component in U_{abc} is transformed

into (6) by transformation matrix (5).

 $T_{abc/\alpha\beta} = \frac{2}{3} \begin{vmatrix} & -\frac{1}{2} & -\frac{1}{2} \\ 1 & \frac{1}{2} & -\frac{1}{2} \\ 0 & \sqrt{3} & -\frac{\sqrt{3}}{3} \end{vmatrix}$ (5)

$$\mathbf{u}_{\alpha\beta}^{+} = \begin{bmatrix} \mathbf{I}_{\alpha\beta} \end{bmatrix} \mathbf{u}_{abc}^{+}$$

$$\mathbf{u}_{\alpha\beta}^{-} = \begin{bmatrix} \mathbf{I}_{\alpha\beta} \end{bmatrix} \mathbf{u}_{abc}^{-}$$
(6)

 $u^+_{\alpha\beta}$ is the positive sequence component of voltage, $u_{\alpha\beta}$ is the negative sequence component of and voltage.Finally, we can compute these matrices to obtain

the following expression:

$$\boldsymbol{u}_{\alpha\beta}^{+} = \begin{bmatrix} \boldsymbol{T}_{\alpha\beta^{+1}} \end{bmatrix} \boldsymbol{u}_{\alpha\beta}; \begin{bmatrix} \boldsymbol{T}_{\alpha\beta^{+1}} \end{bmatrix} = \frac{1}{2} \begin{bmatrix} 1 & -q \\ q & 1 \end{bmatrix} \quad (7)$$

$$\boldsymbol{u}_{\alpha\beta}^{-} = \begin{bmatrix} \boldsymbol{T}_{\alpha\beta^{+1}} \end{bmatrix} \boldsymbol{u}_{\alpha\beta}; \begin{bmatrix} \boldsymbol{T}_{\alpha\beta^{+1}} \end{bmatrix} = \frac{1}{2} \begin{bmatrix} 1 & q \\ q & 1 \end{bmatrix} \quad (8)$$

 $\boldsymbol{a}_{\alpha\beta} - \left[\boldsymbol{a}_{\alpha\beta^{-1}}\right] \boldsymbol{a}_{\alpha\beta}; \left[\boldsymbol{a}_{\alpha\beta^{-1}}\right] = \frac{1}{2} \left[-q \quad 1\right]$ $q\left(q = e^{\frac{-jw}{2}}\right)$ represents the phase shift operation of lag

 90° , which can obtain the quadrature axis component of the input waveform when applied to the time domain.

 u'_{α} , u'_{β} , qu'_{α} π qu'_{β} is the direct and quadrature axis signal in the α and β components of the input vector. They are generated by two second order generalized integrators - orthogonal signal generators. These signals serve as input signals to the Positive/Negative Sequence Component Computing Module (PNSC). The formulas for

each order component on axis $\alpha\beta$ are (7) and (8). The positive and negative sequence components of the fundamental frequency are obtained by decoupling PNSC, and the phase and frequency of the positive sequence components of the fundamental frequency are detected by phase-locked loop.Finally, the asymmetric input voltage vector is transferred to the positive sequence component by a biquadratic generalized integrator. The function formula (9) is as follows:

$$\boldsymbol{u}_{\alpha\beta}^{\dagger} = \begin{bmatrix} \boldsymbol{T}_{\alpha\beta}^{\dagger} \end{bmatrix} \boldsymbol{u}_{\alpha\beta} = \frac{1}{2} \frac{k\omega}{s^{2} + k\omega s + \omega^{2}} \begin{bmatrix} s & -\omega \\ \omega & s \end{bmatrix} \boldsymbol{\omega} \quad (9)$$

The structure diagram of the three-phase phase-locked loop of the two-order generalized integrator is shown in Figure 2:



Fig.2 Structure diagram of bi-second-order generalized integral phase-locked loop

In this paper, the frequency locked loop is used instead of the phase locked loop, and the signal U α and U β as the input of the frequency locked loop system of the biorder generalized integrator have the same frequency.The frequency difference signals generated by α and β signal generators are combined using the method of calculating the mean error signal:

$$\varepsilon_{f} = \frac{\varepsilon_{f(\alpha)} + \varepsilon_{f(\beta)}}{2} = \frac{\varepsilon_{\alpha} q u_{\alpha} + \varepsilon_{\beta} q u_{\beta}}{2}$$
(10)

 \mathcal{E}_{f} is the combined frequency difference signal.

 $\mathcal{E}_{f(\alpha)}$ and $\mathcal{E}_{f(\beta)}$ are α and β frequency difference signals respectively. The two order generalized integrator frequency-locked loop realizes the decoupling estimation of the symmetric components of the three-phase voltages and the estimation of the grid frequency at the α and β reference coordinates. The detection and control algorithm can realize the fast and accurate detection of grid faults, especially the asymmetric faults on the grid side, which judge the faults in the first time.

4 Reactive power compensation control of grid side converter

When grid voltage sag occurs, grid side will produce a lot of reactive power demand. The directional dq axis decoupling control is adopted for the PWM converter on the grid side. Axis d and Axis q control DC voltage and reactive current respectively, so the reactive power compensation of grid voltage can be realized by adjusting the current of Axis q.

When the grid-side voltage drops, the grid-side converter is in the state of reactive compensation. At this time, the current reference value of dq axis is reset, and the active current i_{reactive} for reactive compensation is calculated through the PI regulator of the outer loop of the grid voltage. With the help of formula (11), the maximum active current i_{active} provided by the outer voltage loop in reactive power compensation is calculated, and the value

of i_{active} and the reference value of the original active current i_{gdref} is logically judged. If i_{active} is large, it means that the reactive power compensation capacity of the outer voltage loop is enough to stabilize the DC voltage and ensure the stability of the system. If i_{active} is small, it means that the reactive power compensation ability of the outer voltage loop cannot stabilize the DC voltage. In order to ensure the stability of the system, it is necessary to input the unloading circuit through the PI regulator to consume the excess reactive power.

$$i_{active} = \sqrt{i_{\max}^2 - i_{reactive}^2} \tag{11}$$

 i_{max} is the maximum current value that the grid side converter is allowed to pass through in the formula.

The positive sequence voltage of the grid side is extracted and compared with 90% of the rated voltage. When the grid side converter detects that the positive sequence voltage of the grid is less than 90% of the rated voltage, it delays 30ms to enter the low voltage traverse. During low voltage crossing, reactive current takes precedence over control. The grid side converter sends reactive current according to the depth of voltage sag to support grid voltage recovery. After the grid voltage is restored, the converter exits the low-voltage through-through operation mode with a delay of 20ms.

The reactive current control mode during low voltage traverse is as follows:

$$i_{reactive} = 2 (0.9 - U_{positive}) I_N$$
(12)

 $I_{\rm N}$ is the rated current of the wind farm.

In case of symmetrical sag of grid voltage, the fluctuation range of reactive current during low voltage crossing is: [0-50%];In the case of asymmetric sag of grid voltage, the range of reactive current fluctuation during low voltage crossing is [0-40%].Through the above analysis, the control strategy of the network side converter is improved.It can provide reactive power compensation and stabilize the power supply voltage in time when the measuring voltage falls down.The control schematic diagram of the improved grid-side converter is shown in Figure 3.



Fig.3 Control schematic diagram of grid-side converter

5 Simulation analysis of optimized low voltage traverse control strategy

Fault condition: the direct drive permanent magnet wind power generation system runs at the experimental wind speed (9.3m/s) and the voltage at the middle side of the grid runs at 28.5kV. The three-phase symmetrical voltage drop fault occurs in the grid at 1.5s.The failure duration was 1.85s and the drop depth was 50%. The simulation graph of the control theory adopted in this paper is as follows.



Grid side power factor



Fig.4 The simulation waveform of the improved control strategy when the voltage drops by 50%.

After the improved control strategy, the grid side active power decreases by about 58.26%, the reactive power compensation decreases by about 53.39%, and the power factor decreases by about 33% during the same grid fault period. The active and reactive power parameters have reached the standard. Although some power factor is lost, it can ensure that the fan does not run off the grid during the power grid failure, and guarantee the high quality power output.

6 Conclusion

In this paper, the mathematical model and operation control method of direct drive permanent magnet synchronous wind turbine are studied.Based on the analysis of the reasons of DC capacitive voltage fluctuation caused by the power imbalance on both sides of the power network and the conventional DC braking control strategy, the advantages and disadvantages of the conventional control strategy of the direct-drive permanent magnet wind turbine in the process of low voltage crossing are summarized. This paper selects a new coordinated control strategy for low voltage traversing, which includes unload resistance control, phase-locked control, positive and negative voltage sequence extraction and grid side reactive power control. This strategy improves the voltage stability of the power grid, realizes the safe crossing of the wind turbine during the period of low voltage failure, and improves the stability and practicability of the system.By building a simulation model, the conventional control method is not in line with the actual requirements.It realizes the problem of low penetration but can not be connected to the grid, which verifies the effectiveness and superiority of the new LVRT control strategy.

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